

Docket No.: 61352-057



**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of	:	Customer Number: 20277
Michihito UEDA, et al.	:	Confirmation Number: 6300
Serial No.: 10/701,082	:	Group Art Unit: Unknown
Filed: November 05, 2003	:	Examiner: Unknown
For:	:	
STOCHASTIC PROCESSOR AND STOCHASTIC COMPUTER USING THE SAME		

**INFORMATION DISCLOSURE STATEMENT**

Mail Stop IDS  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

The relevance of the article "A CMOS Stochastic Associative Processor Using PWM Chaotic Signals" is discussed in the present specification.

10/701,082

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

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**Date: July 16, 2004**



SHEET 1 OF 1

**INFORMATION DISCLOSURE  
CITATION IN AN  
APPLICATION**

(PTO-1449)

ATTY. DOCKET NO.  
**61352-057**SERIAL NO.  
**10/701,082**APPLICANT  
**Michihito UEDA, et al.**FILING DATE  
**November 05, 2003**GROUP  
**To be assigned****U.S. PATENT DOCUMENTS**

EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Code <sup>2</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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**FOREIGN PATENT DOCUMENTS**

EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document Country Codes - Number & -Kind Codes (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Figures Appear	Translation	
						Yes	No

**OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)**

EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	
		MORIE ET AL., "A CMOS Stochastic Associative Processor Using PWM Chaotic Signals", IEICE Transactions on Electronics, Vol. E84-C, No. 12, December 2001, pp. 1723-1729	

EXAMINER

DATE CONSIDERED

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.